

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: James D. Ballew et al.

Serial No.: 10/824,874

Filing Date: April 15, 2004

Art Unit: 2181

Confirmation No.: 7117

Examiner: Benjamin P. Geib

Title: ***Coupling Processors to Each Other for High Performance Computing (HPC)***

MAIL STOP: AMENDMENT

Commissioner of Patents

PO Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Information Disclosure Statement (IDS)

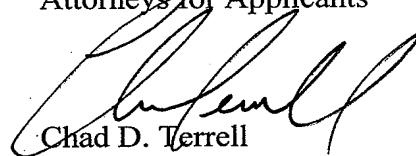
Applicants respectfully request, pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified patent application. References other than U.S. patents and U.S. patent application publications are enclosed for the convenience of the Examiner.

Pursuant to 37 C.F.R. §§1.97(g) and (h), Applicants make no representation that these documents qualify as prior art, that these documents are material to patentability of the present application, or that a search has been made.

This IDS is being submitted before the first Office Action in the above-identified patent application (after the filing of an RCE). However, the Commissioner is hereby authorized to charge any additional fees and credit any overpayments to Deposit Account No. 02-0384 of BAKER BOTTS L.L.P.

Respectfully submitted,

Baker Botts L.L.P.
Attorneys for Applicants



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Date: July 15, 2010

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